

SoK: Attestation in Confidential Computing

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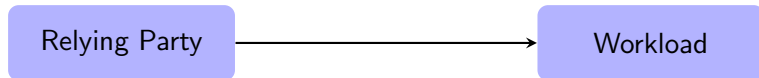
Outline

- 1 Problem Statement
- 2 Contributions
- 3 Summary

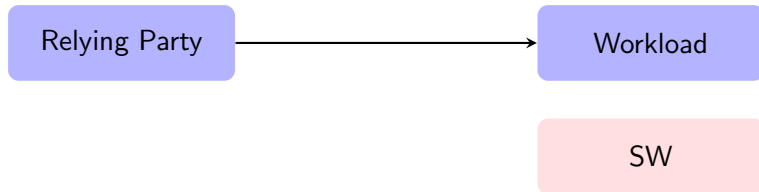
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Relying Party

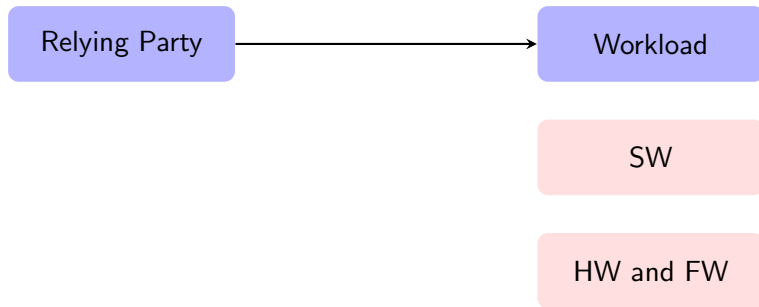
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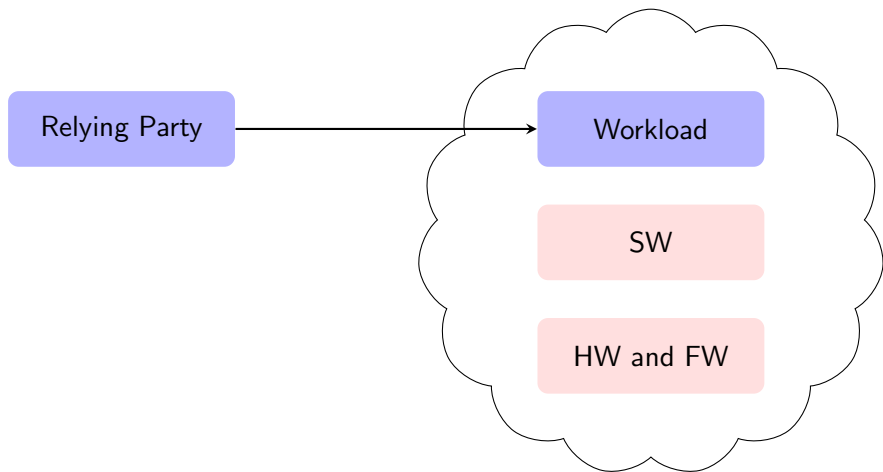
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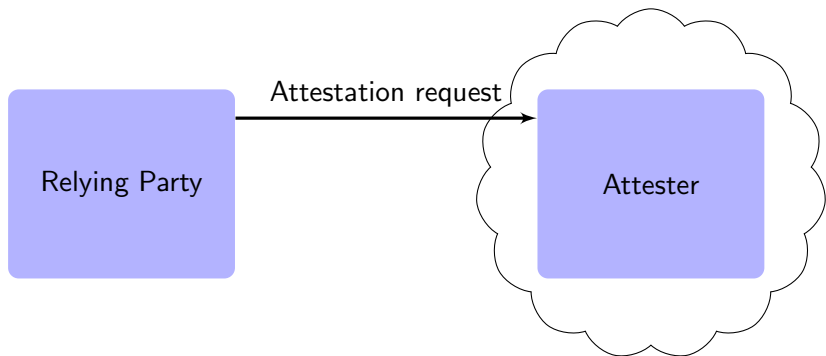
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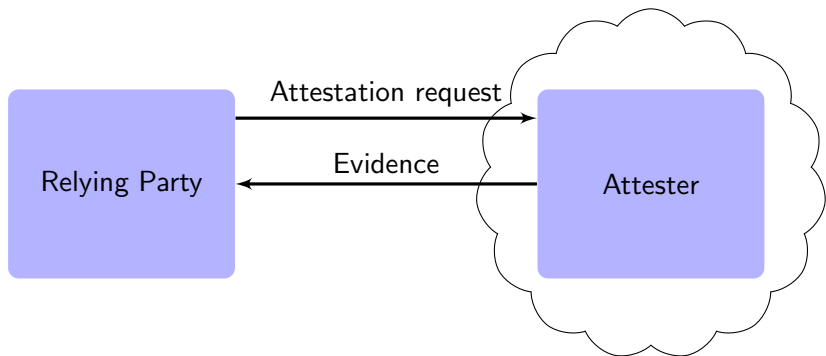
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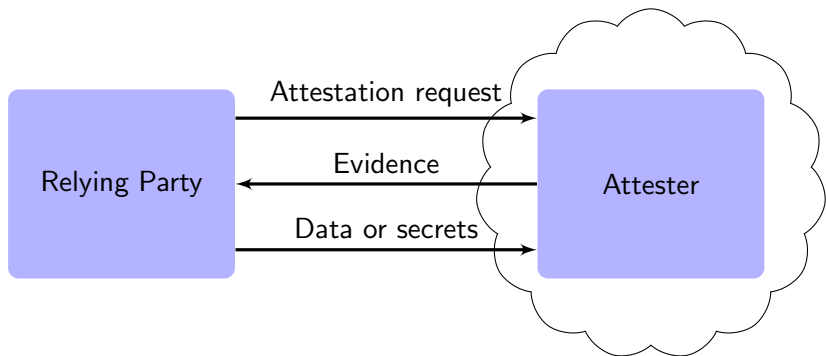
Attestation



Attestation



Attestation



Problem Statement

Holistic view of attestation

Problem Statement

Holistic view of attestation

TEE-agnostic attestation architecture

Problem Statement

Holistic view of attestation

TEE-agnostic attestation architecture

Mappings to attestation architecture

Problem Statement

Holistic view of attestation

TEE-agnostic attestation architecture

Mappings to attestation architecture

Formal specs

Outline

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2 Contributions

- Holistic View
- TEE-agnostic Architecture
- Mappings
- Formal Specs
- Design and Security Issues: TDX
- Design and Security Issues: SCONE

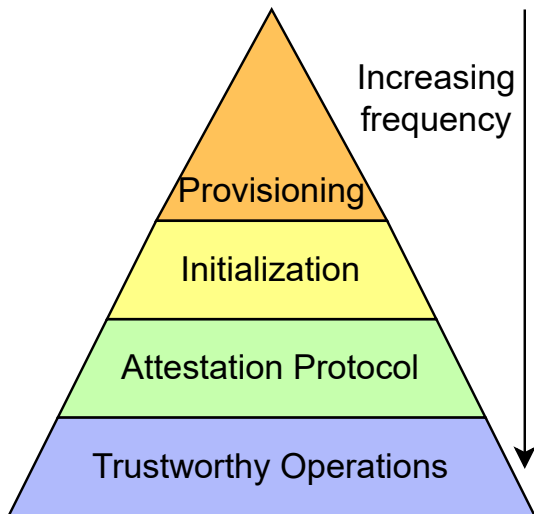
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Holistic View of Attestation



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Attestation Architecture

- Limitations of RATS¹

¹Birkholz et al., *Remote ATtestation procedureS (RATS) Architecture*, 2023.

Attestation Architecture

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 - **Local attestation** out of scope (cannot express Intel's attestation mechanisms)

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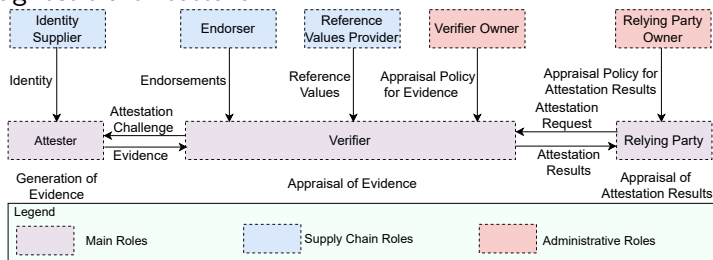
Attestation Architecture

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- TEE-agnostic architecture



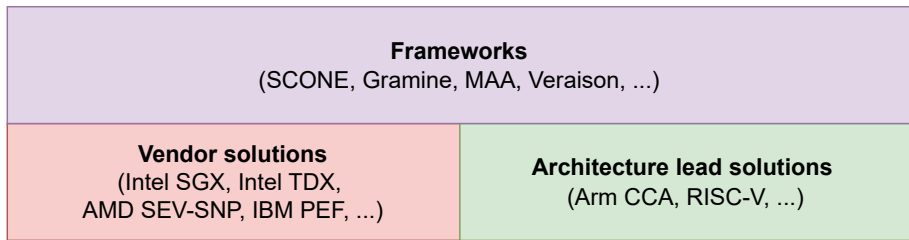
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Outline

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Main Groups for Attestation



Overview of Related Work

Related work				
IETF RATS ²				
Ménétreay et al. ^{3,4}				
Niemi et al. ⁵				

²Birkholz et al., *Remote ATtestation procedureS (RATS) Architecture*, 2023.

³Ménétreay, Göttel, Pasin, et al., "An Exploratory Study of Attestation Mechanisms for Trusted Execution Environments", 2022.

⁴Ménétreay, Göttel, Khurshid, et al., "Attestation Mechanisms for Trusted Execution Environments Demystified", 2022.

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Ménétreay et al. ^{4,5}	Use RATS	Inaccurate for SGX		
Niemi et al. ⁶	Adapted from RATS	Very high level for SGX		

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Overview of Related Work

Related work	Architecture	Mapping to group 1	Mapping to group 2	
IETF RATS ²	Co-developed with DICE ³	No	No	
Ménétreay et al. ^{4,5}	Use RATS	Inaccurate for SGX	No	
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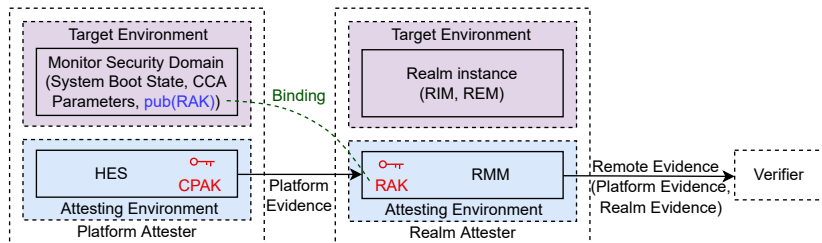
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Arm CCA Attestation Architecture Overview

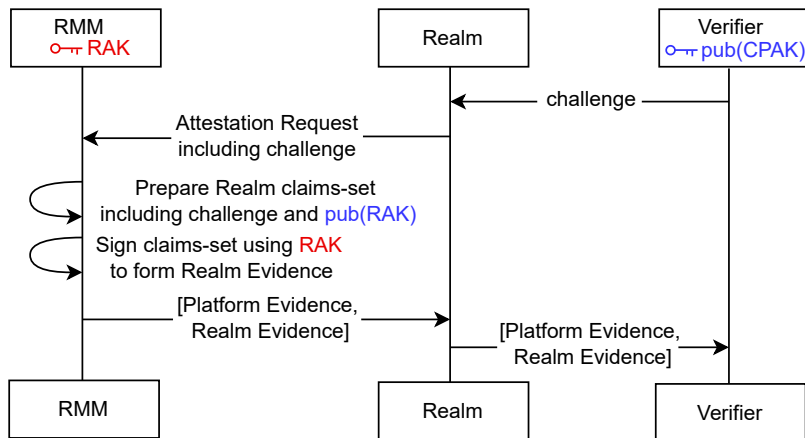


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- TEE-agnostic Architecture
- Mappings
- **Formal Specs**
- Design and Security Issues: TDX
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Arm CCA Evidence Generation



Formal Analysis in ProVerif

- Assumptions
 - Verifier has **preconfigured pub(CPAK)** for signature verification
 - **Secure channel** between HES and RMM to transport the RAK key pair
- Integrity of Platform and Realm Evidence

query *data* : *bitstring* ;
event (*accepted*(*data*)) ==> inj-event (*sent*(*data*)). (1)

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Claimed TCB

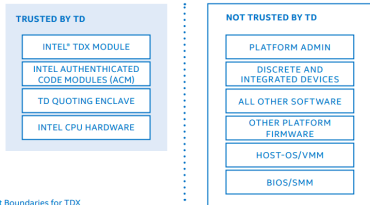
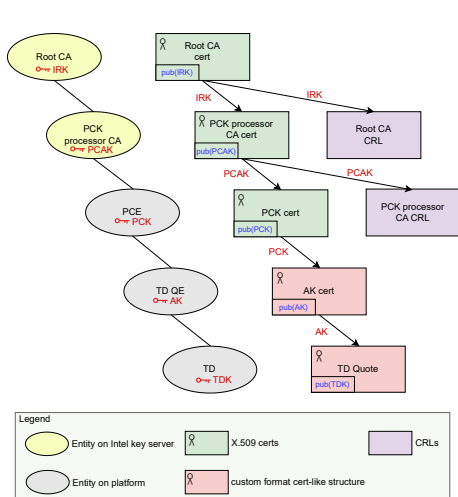


Figure 5.1. Trust Boundaries for TDX



TCB Fixed

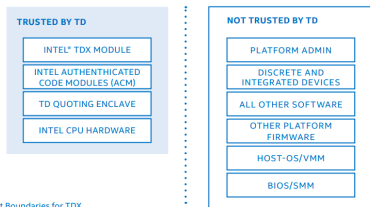


Figure 5.1. Trust Boundaries for TDX

Figure: Old

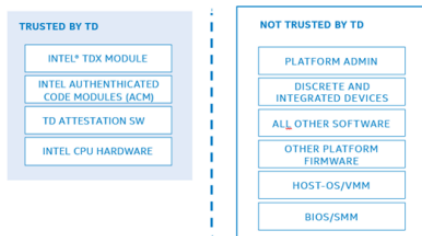
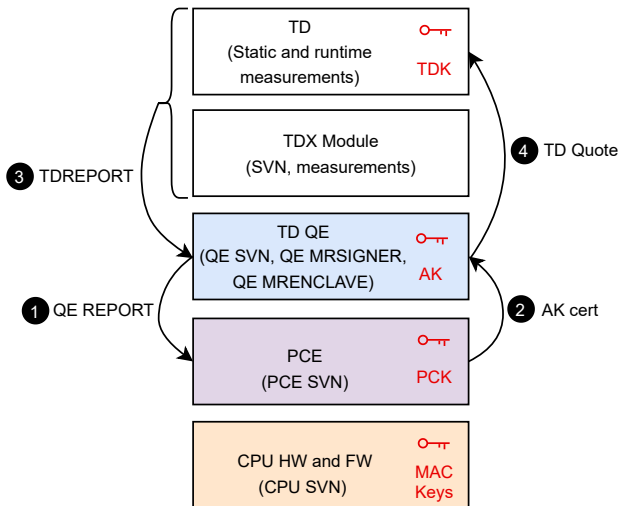


Figure 1 Trust Boundaries for TDX

Figure: Updated

SVN for TD?



Missing Specs

Provisioning phase

Missing Specs

Provisioning phase

Structure of Remote Evidence (TD Quote)

Missing Specs

Provisioning phase

Structure of Remote Evidence (TD Quote)

Structure of AK cert

Missing Specs

Provisioning phase

Structure of Remote Evidence (TD Quote)

Structure of AK cert

KDF for Local Evidence

Outline

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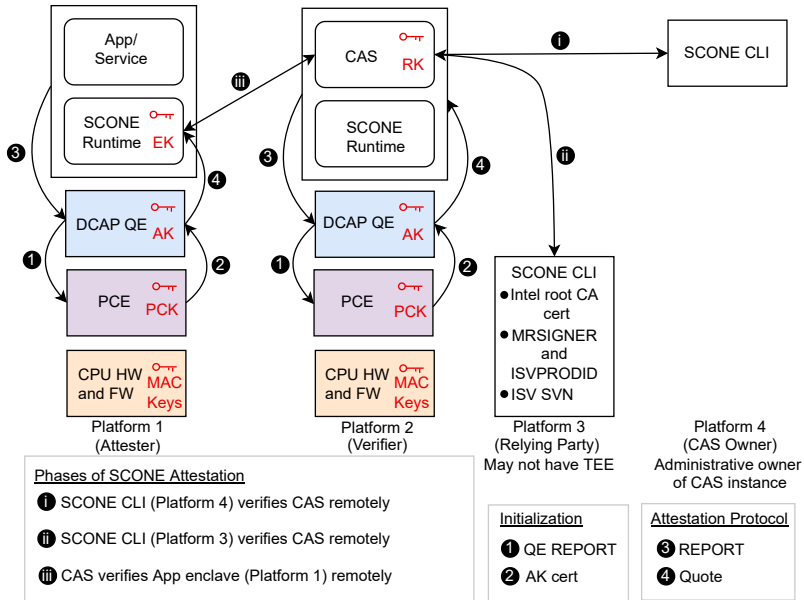
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Order of QE selection

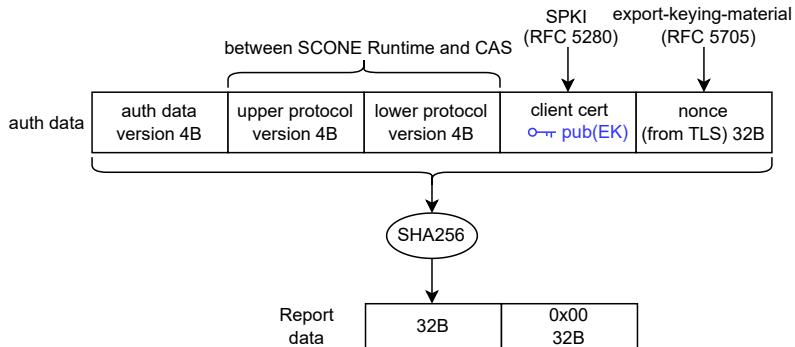
Chosen based on **platform capabilities** (not by **app owner**)

- Perspective 1
 1. DCAP QE (qe3)
 2. SCONE QE + EPID QE
 3. EPID QE
- Perspective 2
 1. DCAP QE (qe3)
 2. EPID QE
 3. SCONE QE (can use only if platform ID is known)
- Perspective 3
 - **Everything (out of EPID, DCAP, SCONE Quote)** that Platform 1 supports is sent to the CAS. So order is not important. CAS decides based on the policy.
 - food for thought: what do we gain?
 - unnecessary overhead without any apparent gain

LA vs. RA



When is a property attested?



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Challenges

ca. 1500 pages of specs of TDX

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Inherits specs from SGX (SDM alone ca. 5000 pages)

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Specs in natural language

Challenges

ca. 1500 pages of specs of TDX

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Specs in natural language

Closed-source nature of SCONE

Take-home

- Towards TEE-agnostic *verification* infrastructure for transparency and interoperability

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- TDX: how do we precisely express trust boundaries?

Take-home

- Towards **TEE-agnostic verification infrastructure** for transparency and interoperability
- **TDX**: how do we precisely express trust boundaries?
- **SCONE**: when do we say that something is attested?

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- **TDX**: how do we precisely express trust boundaries?
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- Lots of work required for **precise** specification and standardization for understanding underlying assumptions

Take-home

- Towards **TEE-agnostic verification infrastructure** for transparency and interoperability
- **TDX**: how do we precisely express trust boundaries?
- **SCONE**: when do we say that something is attested?
- Lots of work required for **precise** specification and standardization for understanding underlying assumptions
 - Integration with TLS (**RA-TLS**)

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- Towards **TEE-agnostic verification infrastructure** for transparency and interoperability
- **TDX**: how do we precisely express trust boundaries?
- **SCONE**: when do we say that something is attested?
- Lots of work required for **precise** specification and standardization for understanding underlying assumptions
 - Integration with TLS (**RA-TLS**)
 - Integration with vTPM

Key References



Birkholz, Henk et al. *Remote ATtestation procedureS (RATS) Architecture*. RFC 9334. Jan. 2023. DOI: 10.17487/RFC9334. URL: <https://www.rfc-editor.org/info/rfc9334>.



Ménétreay, Jämes, Christian Göttel, Anum Khurshid, et al. "Attestation Mechanisms for Trusted Execution Environments Demystified". In: *Lecture Notes in Computer Science (including subseries Lecture Notes in Artificial Intelligence and Lecture Notes in Bioinformatics)* 13272 LNCS (2022), pp. 95–113. ISSN: 16113349. DOI: 10.1007/978-3-031-16092-9_7.



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Trusted Computing Group. *DICE Attestation Architecture*. Tech. rep. 2021. URL: <https://trustedcomputinggroup.org/wp-content/uploads/DICE-Attestation-Architecture-r23-final.pdf>.

Call to Action

- Get **involved**: <https://github.com/CCC-Attestation/formal-spec-TEE>
- Additional information: [link here](#)
- Specify your attestation designs using presented architecture and proposed formalism

